

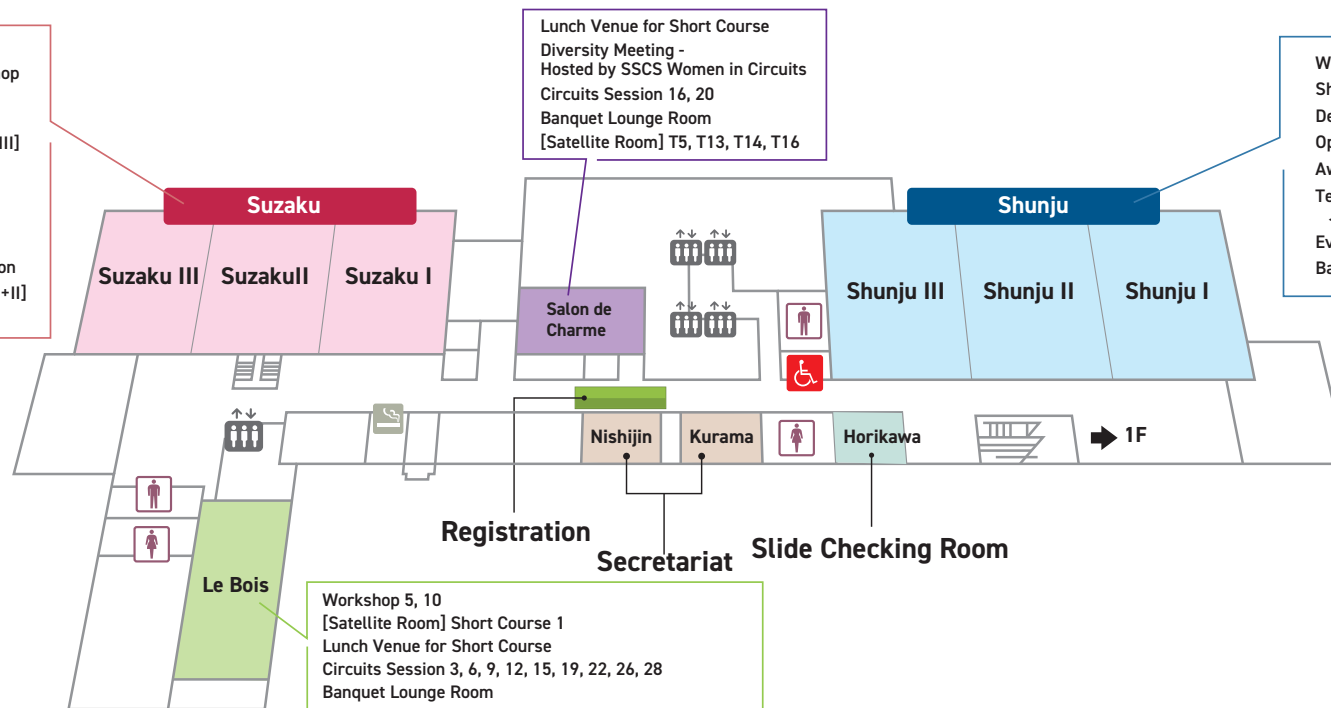
2025 Symposium on VLSI Technology and Circuits - Floor Map -

Special Workshop [Suzaku I]
2025 Silicon Nanoelectronics Workshop
(Day 1) [Suzaku II+III]
(Day 2) [Suzaku III]
2025 Spintronics Workshop [Suzaku III]
Short Course 2 [Suzaku I+II]
[Satellite Room]
Opening and Plenary Session 1
Award and Plenary Session 2
Circuits Sessions / Technology Session
Evening Panel Discussion 2 [Suzaku I+II]
Banquet

Lunch Venue for Short Course
Diversity Meeting -
Hosted by SSCS Women in Circuits
Circuits Session 16, 20
Banquet Lounge Room
[Satellite Room] T5, T13, T14, T16

Workshop 2, 4, 9 [Shunju I] / Workshop 1, 3, 8 [Shunju II+III]
Short Course 1 [Shunju II+III]
Demo Session & Reception [Shunju I+II+III]
Opening and Plenary Session 1 [Shunju I+II+III]
Award and Plenary Session 2 [Shunju I+II+III]
Technology Sessions / Technology Focus Sessions /
Joint Focus Sessions / Circuits Session
Evening Panel Discussion 1 [Shunju II+III]
Banquet

2F



Workshop 5, 10
[Satellite Room] Short Course 1
Lunch Venue for Short Course
Circuits Session 3, 6, 9, 12, 15, 19, 22, 26, 28
Banquet Lounge Room

Workshop 6, 11
[Satellite Room] Workshop 2
[Satellite Room] Short Course 1
Lunch Venue for Short Course
SSCS/EDS Young Professionals and Women in Circuits Mentoring Event
[Satellite Room] T21

Workshop 7, 12
Technology Session 3, 5, 8, 9, 13, 14, 16, 23

1F

