

The IEEE Solid-State Circuits Society

VLSI Circuits Student Travel Grant Award

Application

Deadline: May 2, 2022

The IEEE Solid-State Circuits Society will offer travel grants to help student members who would otherwise not be able to attend VLSI Circuits in 2018. These awards will provide stipends of **up to** USD \$1,000 each and complimentary VLSI Circuits student registration. The winners will be notified by email by May 13, 2022.

Eligibility Requirements

Applicants must be **members of the Solid-State Circuits Society**, have not previously received a Student Travel Grant Award, have been enrolled in a PhD program for at least one year, and be full-time students at the time of their request.

Join SSCS at <http://sscs.org/join>

Application Biographical Information

- a. Name
- b. Mailing Address
- c. Email address
- d. IEEE member number
- e. Name of School and Department
- f. Name of advisor or major professor
- g. Email address of advisor
- h. Grade point average in graduate level courses: _____ out of a possible total of _____.
(Example: 3.5 out of 4.0)

Academic Profile

A one- page summary (up to 250 words) of academic background, including degrees granted and grade point average of each; PhD research topic; future study plan and reason(s) for wanting to attend the Conference.

Recommendation

A one-page recommendation (up to 250 words) from your advisor or major professor. Please note that he or she may recommend only one student in a given year.

Have you been awarded the STGA in the past? If yes, please explain.

Biography:

Submission

Please email all the above information in **one PDF file** to vlsi@vlsisymposium.org no later than May 2, 2022.