

Friday Forum "VLSI for infrastructure and infrastructure for VLSI"

Time	Tapa I & 2	
8:30 AM	Yue Liang (Nvidia)	Forum Opening
8:45 AM	Uwe Kirchner (Infineon)	Green Mobility - Future Concepts for a Sustainable Charging Infrastructure and Energy Delivery
9:30 AM	David Bol (UC Louvain)	Moore's Law and The Pursuit of Efficiency in Information and Communication Technology (ICT): Which Ecological Outcome?
10:15 AM		BREAK
10:30 AM	Kohei Onizuka, (Toshiba) UK	Mixed-signal ICs for Power Electronics: A Key Driver for Bringing Out the Potential of Advanced Power Semiconductor Devices
11:15 AM	Atsutake Kosuge, (Univ. of Tokyo)	Computational Sensing Technologies for Cyber-Physical Infrastructure
12:00 AM		Lunch break
1:00 PM	Andrzej Strojwas, (PDF)	Smart Design and Manufacturing Strategies for New Product Introduction and Volume Production in Leading Edge Technologies
1:45 PM	Hiroshi Akahori, (K)	Utilization of Big Data for Innovation in Semiconductor Manufacturing
2:30 PM		BREAK
2:45 PM	Jeff Cunningham, (NXP)	Creating New Opportunities with Connected Electric Vehicles in Security and Communication
3:30 PM	Sanjiv Mittal, (AMAT)	Leveraging Digital Tools to Implement a Green Vision for Semiconductor Lab to Manufacturing
4:15 PM		Closing