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The 2020 Symposia on VLSI Technology & Circuits Goes Virtual

Against the current backdrop of the coronavirus pandemic, the Symposia theme looks ahead to “The Next 40 Years of VLSI for Ubiquitous Intelligence,” reflecting the conference’s critical role in developing the future ecosystem of technology building blocks, circuits, and applications needed to realize a truly connected society

PISCATAWAY, NJ (APRIL 13, 2020) – For the first time in its 40-year history, the 2020 Symposia on VLSI Technology & Circuits will be held as a virtual conference due to concerns over the global coronavirus (COVID-19) pandemic. Despite the change in format, the Symposia program promises to deliver a unique perspective on the integration of advanced technology developments, innovative circuit design, and the applications they enable – such as machine learning, IoT, artificial intelligence, wearable/implantable biomedical applications, big data, cloud / edge computing, virtual reality (VR) / augmented reality (AR), robotics, and autonomous vehicles – the emerging technology ecosystem needed to realize the conference theme of **“The Next 40 Years of VLSI for Ubiquitous Intelligence.”**

The now-virtual Symposia will be held from June 14 – 19, 2020 with a comprehensive program of technical presentations, panel discussions, focus sessions, short courses, and the popular “Friday Forum” a focused discussion on **“Technologies & Circuits for Edge Intelligence.”** The Symposia is the microelectronics industry’s premiere international conference, integrating technology, circuits, and systems with a range and scope unlike any other conference, charting the roadmap for the connected devices and systems that will drive the next stage of human interaction.

“Although it was a difficult decision to switch to a virtual conference format this year, the health and safety of our attendees is our first priority,” said Chorng-Ping Chang, Symposium on VLSI Technology general chair. “However, with the virtual format, we will still be able to share a comprehensive program, including advanced device technologies for AI, the IoT, and machine learning, as well as next-generation memory devices, cutting-edge low-power sensors, and the innovative ‘beyond CMOS’ technologies that are emerging as a critical element in our connected world.”

“While we will certainly miss seeing our Symposia colleagues in person this year, the ongoing global health situation made the decision necessary,” explained Ken Chang, Symposium on VLSI Circuits general chair. “We are looking forward to a wide range of online presentations as

part of the Circuits program, including the next wave of innovative computing systems and architectures required for the evolution of AI, machine learning, ‘big data’ management, autonomous transportation, and, perhaps most importantly in the current environment, advanced biomedical applications.”

Details about the Symposia program schedule and registration will be available on the Symposia website by late April: <https://vlsisymposium.org/registration/>

The 2020 Symposium on VLSI Technology & Circuits will be held online from June 14-19, 2020, including three Short Courses and a special Friday Forum dedicated to “*Technologies & Circuits for Edge Intelligence*.” The two Symposia have been held together since 1987, providing an opportunity for the world’s top device technologists, circuit and system designers to exchange leading edge research on microelectronics technology, with alternating venues between Hawaii and Japan. A single registration enables participants to participate in both Symposia.

Sponsoring Organizations

The Symposium on VLSI Technology is sponsored by the IEEE Electron Devices Society and the Japan Society of Applied Physics, in cooperation with the IEEE Solid State Circuits Society.

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Further Information and Registration

Visit: <http://www.vlsisymposium.org>.

Symposia Contact

Deidre Artis | IEEE
d.artis@ieee.org

Media Contact

Chris Burke | BtB Marketing Communications
chris.burke@btbmarketing.com | +1-919-872-8172

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