



*FOR IMMEDIATE RELEASE – 2 June, 2020*

## **The 2020 Symposia on VLSI Technology & Circuits Program to Include 25 Fully-Interactive ‘Executive Sessions’ to Engage Participants in Discussions About the Future of VLSI**

*With the virtual format, the 2020 Symposia registration fees have been reduced to \$300 (\$150 for students), making the conference program accessible to a wider audience than ever before.*

PISCATAWAY, NJ (JUNE 5, 2020) – For the first time in its 40-year history, the 2020 Symposia on VLSI Technology & Circuits, the microelectronics industry’s premiere international conference integrating technology, circuits, and systems, will feature 25 live, interactive “Executive Sessions.” These highly-interactive events will enable participants to discuss current technologies, ask questions of experts, and contribute their insights. These 50-minute sessions will include discussions with authors and session chairs on key challenges and opportunities for the industry. Online polls will allow the opportunity for participants to interact with the presenters.

The now-virtual Symposia program, organized around the theme of “**The Next 40 Years of VLSI for Ubiquitous Intelligence**,” will be held from June 15 – 19, 2020, with more than 40 live interactive sessions, workshops, panel sessions, and plenary talks, presented in a range of time slots to be convenient for the global VLSI community.

In addition to the live events, the Symposia will feature more than 200 technical papers, three Short Courses, a joint Technology/Circuits panel discussion, a luncheon presentation, and the “Friday Forum,” presented in an on-demand format. The full program for the Symposia can be found here: [vlsisymposium.org/program/](https://vlsisymposium.org/program/)

“The Symposia Technical Program Committees have done a tremendous job in developing a truly interactive program for this year’s virtual Symposia, especially with the new Executive Sessions,” said Chorng-Ping Chang, Symposium on VLSI Technology general chair. “Despite having to quickly pivot to a virtual format, we believe that our program this year is an example of how the VLSI community can set a new standard for interactivity and connectivity in a virtual conference that provides participants a real voice and a global vision in developing the future of our industry.”

“One of the benefits of converting the Symposia to a virtual event is that the registration fees have been dramatically reduced to just \$300, and just \$150 for students,” explained Ken Chang, Symposium on VLSI Circuits general chair. “We want to bring together the entire VLSI community, and we trust that the exciting technical program, the new interactive events, and these reduced registration fees will help us to achieve this goal.”

The Symposia’s virtual program promises to deliver a unique perspective on the integration of advanced technology developments, innovative circuit design, and the applications they enable – such as machine learning, IoT, artificial intelligence, wearable/implantable biomedical applications, big data, cloud / edge computing, virtual reality (VR) / augmented reality (AR), robotics, and autonomous vehicles – the emerging technology ecosystem needed to realize the conference theme “*The Next 40 Years of VLSI for Ubiquitous Intelligence.*”

**Executive Session Topics include:**

- Advanced CMOS (two sessions)
- Sensor Systems
- Memory (three sessions)
- Intelligent Computing
- Analog Building Blocks
- Adaptive Systems
- Heterogeneous Integration (two sessions)
- Power
- 3D Packaging
- Sensors
- Ultralow Energy Systems
- Ferroelectrics
- Advances in Clocking & Data Converters
- Robust Computing
- Artificial Intelligence (AI)/Machine Learning (ML)
- Sensing Interfaces & Building Blocks
- Devices & Circuits for Advanced Communications
- MRAM
- High Speed Circuits, Systems, & Devices
- New Devices & Applications
- Silicon Photonics & DTCO

Details about the Symposia [program](#) schedule and [registration](#) are available on the Symposia website.

The 2020 Symposium on VLSI Technology & Circuits will be held online from June 15-19, 2020, including three Short Courses and three Workshop panel sessions. The two Symposia have been held together since 1987, providing an opportunity for the world’s top device technologists, circuit and system designers to exchange leading edge research on microelectronics technology, with alternating venues between Hawaii and Japan. A single registration enables participants to participate in both Symposia.

**Sponsoring Organizations**

The Symposium on VLSI Technology is sponsored by the IEEE Electron Devices Society and the Japan Society of Applied Physics, in cooperation with the IEEE Solid State Circuits Society.

The Symposium on VLSI Circuits is sponsored by the IEEE Solid State Circuits Society and the Japan Society of Applied Physics, in cooperation with the Institute of Electronics, Information and Communication Engineers.

**Further Information and Registration**

Visit: <http://www.vlssymposium.org>.

**Symposia Contact**

Deidre Artis | IEEE

[d.artis@ieee.org](mailto:d.artis@ieee.org)

**Media Contact**

Chris Burke | BtB Marketing Communications

[chris.burke@btbmarketing.com](mailto:chris.burke@btbmarketing.com) | +1-919-872-8172

###