**Symposium Scope**

The VLSI Technology symposium calls for papers in the following areas:
- CMOS Technology, Microprocessors and SoCs
- Stand-Alone and Embedded Memories
- Technologies for Quantum Computing and New Computing
- RF / Analog / Digital Technologies and Sensors
- Process and Material Technologies
- Advanced Packaging Technologies
- Photonics Technology and Imaging Technology
- Beyond CMOS Devices, incl. Cryo-CMOS Technology

**Focus Sessions**

In addition to the topics mentioned above, the Symposium will highlight the following Focus Sessions:
- Future device and process/material for new computing
- Advanced memory technology
- Advanced PKG and 3D integration (Joint)
- Photonics interconnect and compute (Joint)

**Short Courses**

Three full-day short courses will offer in-depth lectures on the following topics:
- Advanced process and device technology toward 2nm-CMOS and emerging memory (Technology)
- Computing paradigm shift including memory and new devices (Joint)
- Advanced sensor circuits and systems for IoT (Circuit)

**Forum**


**Secretariat for VLSI Symposia**

(Asia and Japan) c/o JTB Communication Design, Inc., Tokyo, Japan
vlsisymp@ibtcom.co.jp
(America and Europe) IEEE Meetings, Conferences & Events (MCE), Piscataway, NJ USA +1 732 465 6620, vlsisympsymposium@ieee.org

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**Evening Panel Discussions**

All attendees are invited to join evening panel on:
- 3D/Heterogeneous integration including democratization
- New Generation Chip Makers vs. the Incumbents?
- Science, Technology and Educations for New Normal Society

**Symposia Demo Session**

All demo will be video-based and open to a wide range of authors. The demo session will provide the opportunity to see pre-recorded demonstrations and prototypes of research projects, corresponding to all relevant topics of the Conference. The most attractive and innovative demo will receive the award.

**Workshop**

Enthusiastic organizers will provide the additional learning opportunities of VLSI related topics for participants. The topics of the workshops will be announced at the end of January.

**Best Student Paper Award**

The selection will be based on quality and presentation of the paper at the Symposium. The winning student will be presented a monetary award and a certificate at the 2022 Symposium opening session. The student must be enrolled as a full-time student at the time of submission, be the leading author and presenter of the paper, and indicate when submitting the paper that the paper should be considered for the award.

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**Paper Submission**

Prospective authors must submit two-page papers and abstracts using the Symposia’s website, www.vlsisympsymposium.org. Accepted papers will be published as submitted, with no revisions permitted. Authors must follow detailed instructions provided within the “Authors” section of the website, including the Authors’ Guide and Pre-Publication Policy. All speakers can join on-line if they wish. Extended versions of selected papers from the Symposium would be published in the IEEE Transactions on Electron Devices.

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VLSI 2021 will be held in a fully virtual format due to COVID-19.

Paper Submission Deadline: 23:59 JST Monday, February 8, 2021
For detail, please visit: www.vlsisympsymposium.org

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**Third Announcement and Call for Papers**

**2021 Symposium on VLSI Technology**
~VLSI Systems for Lifestyle Transformation~

Kyoto, Japan
Sunday – Saturday, June 13-19, 2021 JST
Workshops
Short Courses
Technical Sessions
Forum