2021 SYMPOSIUM ON VLSI TECHNOLOGY
~VLSI Systems for Lifestyle Transformation~

RIHGA Royal Hotel Kyoto, Japan
Monday – Thursday, June 14-17, 2021

Workshops
Short Courses
Technical Sessions
Forum

Depending on the situation of COVID-19, virtual platform will be prepared.

Paper Submission Deadline: 23:59 JST Monday, February 8, 2021
For detail, please visit: www.vlsisymposium.org

Evening Panel Discussions
All attendees are invited to join evening panel on:
- 3D/Heterogeneous integration including democratization
- Circuit Evening Panel (Theme will be determined)
- Science, Technology and Educations for New Normal Society

Symposia Demo Session
All demo will be video-based and open to a wide range of authors. The demo session will provide the opportunity to see pre-recorded demonstrations and prototypes of research projects, corresponding to all relevant topics of the Conference. The most attractive and innovative demo will receive the award.

CALL FOR WORKSHOP
The Symposium will provide valuable opportunities for volunteers/applicant to host short workshops. The call for proposals for those Workshops are now open. You organize the workshop! Check the details in http://vlsisymposium.org/s_workshop.html

Best Student Paper Award
The selection will be based on quality and presentation of the paper at the Symposium. The winning student will be presented a monetary award and a certificate at the 2022 Symposium opening session. The student must be enrolled as a full-time student at the time of submission, be the leading author and presenter of the paper, and indicate when submitting the paper that the paper should be considered for the award.

Paper Submission
Prospective authors must submit two-page papers and abstracts using the Symposia’s website, www.vlsisymposium.org. Accepted papers will be published as submitted, with no revisions permitted. Authors must follow detailed instructions provided within the “Authors” section of the website, including the Authors’ Guide and Pre-Publication Policy. All speakers can join on-line if they wish.

Extended versions of selected papers from the Symposium would be published in the IEEE Transactions on Electron Devices.

Symposium Scope
The VLSI Technology symposium calls for papers in the following areas:
- CMOS Technology, Microprocessors and SoCs
- Stand-Alone and Embedded Memories
- Technologies for Quantum Computing and New Computing
- RF / Analog / Digital Technologies and Sensors
- Process and Material Technologies
- Advanced Packaging Technologies
- Photonics Technology and Imaging Technology
- Beyond CMOS Devices, incl. Cryo-CMOS Technology

Focus Sessions
In addition to the topics mentioned above, the Symposium will highlight the following Focus Sessions:
- Future device and process/material for new computing
- Advanced memory technology
- Advanced PKG and 3D integration (Joint)
- Photonics interconnect and compute (Joint)

Short Courses
Three full-day short courses will offer in-depth lectures on the following topics:
- Advanced process and device technology toward 2nm-CMOS and emerging memory [Tentative] (Technology)
- Computing paradigm shift including memory and new devices (Joint)
- Advanced sensor circuits and systems for IoT (Circuit)

Forum

Secretariat for VLSI Symposia
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