The 2015 VLSI Symposia technical program consists of overlapping sessions from June 15 – 18 (Technology) and June 16 – 19 (Circuits), with more than 200 presentations, short courses and panel discussions by leading researchers and scientists. The program is designed to highlight recent advances in microelectronics technology and circuits, and to promote networking amongst participants.

**Joint Program Highlights**

**Joint Focus Sessions**
Wednesday, June 17 & Thursday, June 18
- Ultra Low Power for IoT
  - Session JFS1 (Wednesday, June 17, 10:30am)
  - Emerging NVM (ReRAM, PCRAM, STT-MRAM)
  - Session JFS2 (Wednesday, June 17, 1:15pm)
- Advanced Technology and Circuits for IoT
  - Session JFS3 (Thursday, June 18, 2:20pm)
- 3D and Heterogeneous Integration
  - Session JFS4 (Thursday, June 18, 4:15pm)

**Joint Evening Panel Discussion**
Tuesday evening, June 16
- Semiconductor Industry in 2020 – Evolution or Revolution?
- Moderators: T. Piliszczuk, SOITEC / J. Tham, Broadcom
- Expert panelists from: CEA-LETI, Google, IBM, imec, NAIST, Stanford University, etc.

**Technology Short Course**
More-than-Moore and More Moore for IoT
Monday, June 15

**Technology Focus Sessions**
7nm Logic Technology and Beyond (incl. Ge, III-V, and TFET)
Session T1 (Tuesday, June 16, 1:30pm)
- 3D Systems and Packaging
Session T5 (Tuesday, June 16, 3:50pm)

**Luncheon**
Thursday, June 18
- DASSAI: Innovating Sake Brewing with Massive Usage of Data and IT
- Kazuhiro Sakurai, Asahi Shuzo Co., Ltd.

**Joint Focus Sessions**
Wednesday, June 17 & Thursday, June 18

**Joint Reception**
Tuesday, June 16

**Joint Banquet**
Wednesday, June 17

**Satellite Workshops**
IEEE Silicon Nanoelectronics Workshop
- June 14-15
Spintronic Workshop on LSI
- June 15

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For complete conference and registration information, visit: [http://www.vlsisymposium.org/](http://www.vlsisymposium.org/)