A track-and-hold is a critical sub-circuit of a wideband pipelined ADC. A 1-GS/s fully differential dual track-and-hold circuit fabricated in an 80-GHz $f_{\text{r}}/f_{\text{max}}$ GaAs HBT production process exhibits an input bandwidth of 8.6 GHz (0.5 Vpp) and 9.3 GHz (small signal). Total harmonic distortion is $-59$ dB for 1-Vpp 1-GHz input, and $-42$ dB for 0.5-Vpp 5-GHz input. The chip occupies 0.9 mm x 1.8 mm active area and consumes 2.2 W from $\pm5$ V.